

TAI-SAW TECHNOLOGY CO., LTD.

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Product Specifications Approval Sheet

Product Name: TCXO SMD 3.2x2.5 16.368MHz

TST Part No.: TX0506B

Customer Part No.:_____

Company:		
Division:		
Approved by:		
Date:		
Checked by:	C. C. Hsu	
Approved by:	1 Jul Juaner	
Date:	12/15/2022	

- 1. Customer signed back is required before TST can proceed with sample build and receive orders.
- 2. Orders received without customer signed back will be regarded as agreement on the specifications.
- 3. Any specifications changes must be approved upon by both parties and a new revision of specifications shall be released to reflect the changes.

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TST DCC Release document

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TCXO SMD 3.2x2.5 16.368MHz

MODEL NO.: TX0506B

REV. NO.: 1

Revise:

Rev.	Rev. Page	Rev. Account	Date	Ref. No.	Revised by
1	N/A	Initial release	12/15/22'	N/A	C.C. Hsu

Ultra Miniature SMD Package Good Frequency Stability

• Moisture Sensitivity Level (MSL) : Level-1

TCXO SMD 3.2x2.5 16.368MHz

MODEL NO.: TX0506B

Features:

Description and Applications:

Surface mount 3.2mmx2.5mm TCXO for use in wireless communications devices Electrical Specifications:

ТХ0506В	Specifications				
Nominal Frequency, Fo	16.368000 MHz				
Storage Temperature Range	-40°C to +85°C				
Operating Temperature Range	-40°C to +85°C				
Power Supply Voltage, Vcc	2.85V +/-5%				
Output Voltage with Load 10pF//10KΩ, Vout	0.8 Vp-p min				
Output Waveform	Clipped Sinewave				
Output Load	10pF//10KΩ				
Power Supply Current, Icc	2.0 mA max				
Frequency Tolerance as Received Ref. to Nominal Frequency	+/- 1.0 ppm max @ 25°C +/- 3°C				
Frequency Deviation after 2 x Reflow Ref. to pre-reflow Freq.	+/- 1.0 ppm max @ 25°C +/- 3°C				
 Frequency Stability a. Vs. Temperature (-30~85°C) Vs. Temperature (-40~-30°C) b. Vs. Load varied 10pF//10KΩ+/-10% c. Vs. Supply Voltage varied Vcc+/-5% 	+/- 0.5 ppm reference to the middle point between minimum and maximum frequency value +/- 5.0 ppm reference to 25°C +/- 0.2 ppm +/- 0.2 ppm				
Start Up Time (90% of final RF level in Vp-p)	2.0 msec max.				
Harmonics	-7.0 dBc max				
Static temperature hysteresis	+/- 0.6 ppm max.				
Aging	+/-1.0 ppm/year @25°C				

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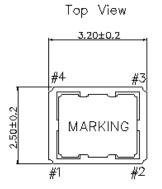
Lead free Lead-free soldering

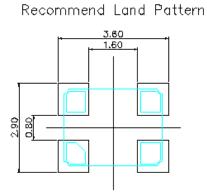
RoHS Compliant

REV. NO.: 1

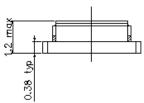
SSB Phase Noise (@1KHz Carrier Offset)	-130 dBc/Hz max
Marking	Laser marking
Mechanical Dimensions (mm)	<u>J</u> J

Jimensions (mm). こしい

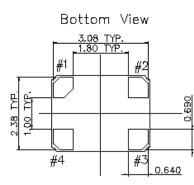




Side View



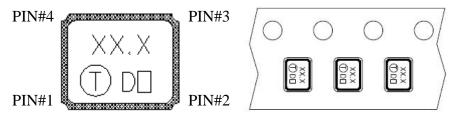
	Pin Connection
#1 pin	GND Recommend
#2 pin	GND
#3 pin	Output
#4 pin	+Vcc



Marking

Line 1: Customer Frequency (16.3)

Line 2: TST Logo + Date Code + Product Code (🗌 is TST internal tracking code, could be a~z and A~Z)



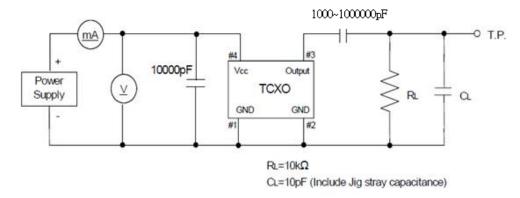
Date Code Table

WK01	WK02	WK03	WK04	WK05	WK06	WK07	WK08	WK09	WK10	WK11	WK12	WK13
Α	В	С	D	E	F	G	Н	I	J	K	L	М
WK14	WK15	WK16	WK17	WK18	WK19	WK20	WK21	WK22	WK23	WK24	WK25	WK26
N	0	Р	Q	R	S	Т	U	V	W	Х	Y	Z
WK27	WK28	WK29	WK30	WK31	WK32	WK33	WK34	WK35	WK36	WK37	WK38	WK39
а	b	с	d	е	f	g	h	i	j	k	I	m
WK40	WK41	WK42	WK43	WK44	WK45	WK46	WK47	WK48	WK49	WK50	WK51	WK52
n	0	р	q	r	S TA	ICAWI		N OPV	CO. ^W LTI	x	у	z

	Product Code					
2013	2015	2017	2019	2021	2023	
2014	2016	2018	2020	2022	2024	

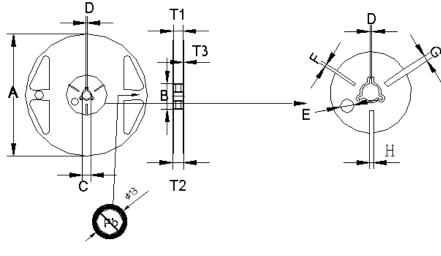
Product Code Table: (Under line With Even Year and Odd Year for Nothing)

Recommended Circuit



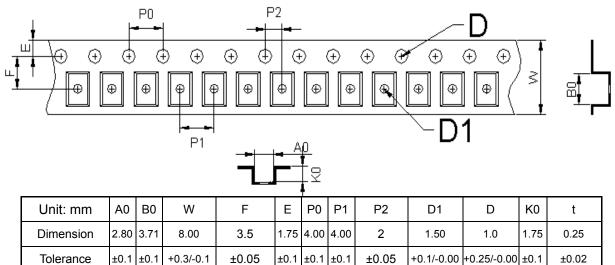
Packing (mm):

1. Reel Dimension

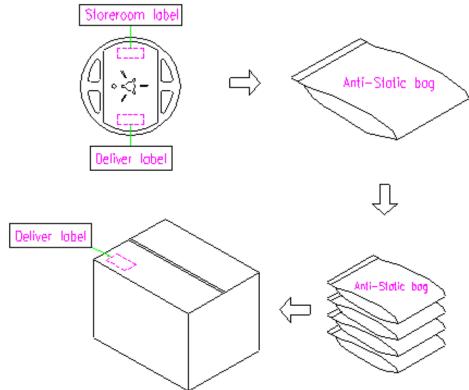


	А	В	С	D	Е	F	Н	G	T2	T1	Т3
Dimensions	180	60	13.0	2.0	9.1	2.9	3.9	4.9	11.4	9.0	1.2
Tolerance	±1.0	+1.0	±0.2	±0.5	±0.5	±0.5	±0.5	±0.5	±1.0	±0.3	±0.1

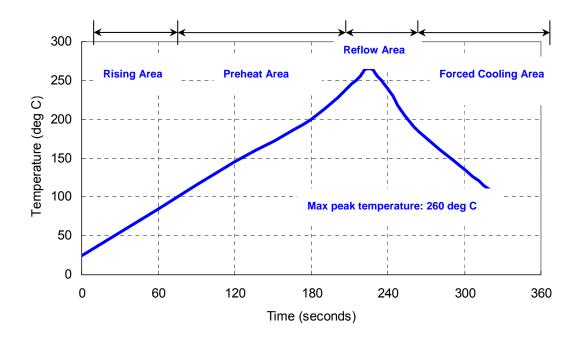
2. Tape Dimension



Packing Quantity/Packing:



Reflow Profile:



Notes of the Usage:

- 1. Touch the solder iron at 260+/-5 deg C onto the leads for 10+/-2 sec max or touch the solder at 350+/-5 deg C onto the leads for 3+/-0.5 sec.
- 2. In the customer's reflow process, if it will remain some mechanical stress at the soldering terminals, also make some cracks on the soldering termination. Some cracks will cause open or short circuit and cause of thermal increasing or smoking. Don't make any excess mechanical stress to soldering points.
- 3. In case of giving a heavy shock to the products, it may make an open or short circuit and cause of thermal increasing and smoking. To avoid heavy shock impact applying to products is strictly required.
- 4. Ultrasonic cleaning should be avoided to prevent damage to the crystal.
- 5. Do Not Use Ultrasonic-Wave Soldering or Wave Solder with Package Immersed in Solder.

Notes of the Storage:

- To keep products under the condition at the room temperature (-5~35 deg C) with normal humidity (45~75%). Absorption of moisture and dewdrop may make inferiority of characteristics and a short circuit.
- Oxidization of terminals shall make the solderability more inferior. Dusts and corrosive gas will make a cause of the open or short circuit. Keep it in the clean place where is not in dusty and no corrosive gas.
- 3. Use the anti-static material to the storage package.
- 4. Don't put any excess weight to the TCXO in the storage process.

- 5. Don't move the product from the cold place to the hot place in the short time, otherwise it may make some dew-drop, then a short circuit may happen in case.
- 6. Storage periods should be maximum 6 months under condition of above item 1 after delivery from TST factory.
- 7. Parts are suggested to be used as soon as the bag is open due to the terminal solderability.
- 8. If you have to keep parts without using after opening the bag, please put the drying agent in the bag, fold the bag and keep it in the place where temperature and humidity are controlled (nitrogen atmosphere box etc.)

Test name	Test process / method	Reference standard							
Mechanical characteristics									
resistance to Soldering heat (IR reflow)	Temp./ Duration : 265°C /10sec ×2 times Total time : 4min.(IR-reflow)	EIAJED-4701 -300(301)M(II)							
Vibration	Total peak amplitude: 1.5mmVibration frequency: 10 to 2000 HzSweep period: 20 minuteVibration directions: 3 mutually perpendicularDuration: 2 hr / direc.	MIL-STD 202G method 204							
Mechanical Shock	directions : 3 impacts per axis Acceleration : 3000g's, +20/-0 % Duration : 0.3 ms (total 18 shocks) Waveform : Half-sine	MIL-STD 202G method 213							
Solderability	Solder Temperature:265±5°C Duration time: 5±0.5 seconds.	J-STD-002							
Environmental	characteristics								
Thermal Shock	Heat cycle conditions -40 °C (30min) ←→ 85 °C (30min) * cycle time : 10 times	MIL-STD 883G method 1010.8							
Humidity test	Temperature : 85 ± 2 °C Relative humidity : 85% Duration : 96 hours	MIL-STD 202G method 103							
Dry heat (Aging test)	Temperature :125 ± 2 °C Duration :168 hours	MIL-STD 202G method 108A							
Cold resistance (Low Temp Storage)	Temperature : -40 ± 2 °C Duration : 96 hours	IEC 60068-2-1							

Reliability Specifications