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Product Specifications Approval Sheet

Product Name: TCXO	SMD 2.0x1.6 38.4	MHz
TST Part No.: TX0908	BCA10BH	
Customer Part No.:		
Company:		
Division:		
Approved by:		
Date:		
Checked by:	Chia Haur Rau	CH
Approved by:	Kelly Huang	Kelly Guang
Date:		

- 1. Customer signed back is required before TST can proceed with sample build and receive orders.
- 2. Orders received without customer signed back will be regarded as agreement on the specifications.
- 3. Any specifications changes must be approved upon by both parties and a new revision of specifications shall be released to reflect the changes.



MODEL NO.: TX0908CA10BH REV. NO.: 1

Revise:

Rev.	Rev. Page	Rev. Account	Date	Ref. No.	Revised by
1	N/A	Initial release	03/27/23'	N/A	Chia Haur Rau



TAI-SAW TECHNOLOGY CO., LTD.

TCXO SMD 2.0x1.6 38.4MHz

MODEL NO.: TX0908CA10BH REV. NO.: 1

Features:

- Ultra Miniature SMD Package
- Good Frequency Stability
- Good Phase Noise Response
- Moisture Sensitivity Level (MSL): Level-1
- Hermetically sealed

RoHS Compliant Lead free ead-free soldering

Description and Applications:

Surface mount 2.0mmx1.6mm TCXO for use in wireless communications devices

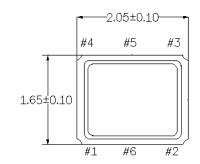
Electrical Specifications:

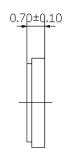
TX0908CA10BH	Specifications					
Nominal Frequency, Fo	38.400000 MHz					
Storage Temperature Range	-40°C to +85°C					
Operating Temperature Range	-30°C to +85°C					
Power Supply Voltage, Vcc	3.3V +/- 5% (Nominal to 3.3 V)					
Output Voltage with Load 10pF//10KΩ, Vout	0.8 Vp-p min					
Power Supply Current, Icc	2.0 mA max					
Output Wave form	Clipped Sinewave					
Output Load	10kΩ//10pF					
Frequency Tolerance as Received Ref. to Nominal Frequency	+/- 0.5 ppm max @ 25°C +/- 3°C					
Frequency Deviation after 2 x Reflow Ref. to pre-reflow Freq.	+/- 1.0 ppm max @ 25°C +/- 3°C					
Frequency Stability a. Vs. Temperature (-30~85°C) b. Vs. Load varied 10pF//10KΩ+/-10% c. Vs. Supply Voltage varied Vcc+/-5%	+/- 0.5 ppm (reference to the middle point between minimum and maximum frequency value) +/- 0.2 ppm +/- 0.2 ppm					
Start Up Time (90% of final RF level in Vp-p)	2.0 msec max.					
Aging	+/-1.0 ppm/ year					
Harmonics	-5.0 dBc max					
SSB Phase Noise(@1KHz Carrier Offset) (@10KHz Carrier Offset)	-130 dBc/Hz max -145 dBc/Hz max					
Marking	Laser marking					

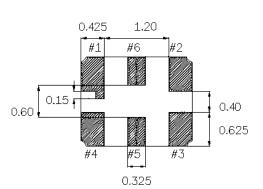
TAI-SAW TECHNOLOGY CO., LTD.

TST DCC Release document

Mechanical Dimensions (mm):

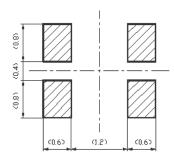






	Pin Connection			
#1	GND			
#2	GND			
#3	Output			
#4	+Vcc			
#5	No connect			
#6	No connect			

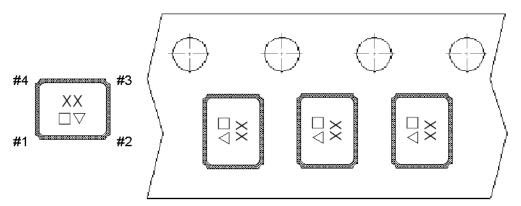
Recommended Land Pattern: (unit: mm)



Marking:

Line 1: Frequency (38)

Line 2: Product Code : ☐ (☐ is TST internal tracking code) + Date Code of Year/Month : ▽



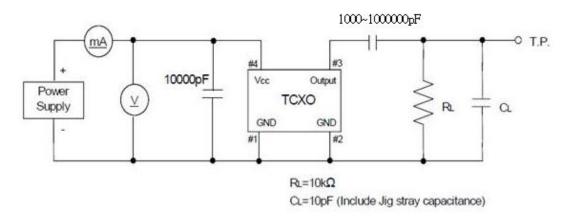
TAI-SAW TECHNOLOGY CO., LTD.

TST DCCRelease document

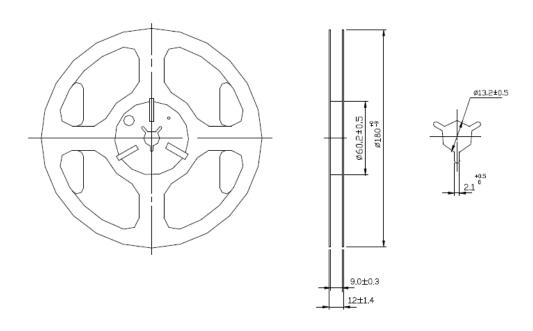
∇ : Date Code Table: Year/Month

Year/Month	1	2	3	4	5	6	7	8	9	10	11	12
2022	Α	В	С	D	Е	F	G	Н	J	K	L	М
2023	N	Р	Q	R	S	Т	U	٧	W	Х	Υ	Z
2024	а	b	С	d	е	f	g	h	i	j	k	m
2025	n	р	q	r	s	t	u	٧	W	х	у	Z
2026	Α	В	С	D	Е	F	G	Н	J	K	L	М
2027	N	Р	Q	R	S	Т	U	٧	W	Χ	Υ	Z
2028	а	b	С	d	е	f	g	h	i	j	k	m
2029	n	р	q	r	S	t	u	٧	W	х	у	Z

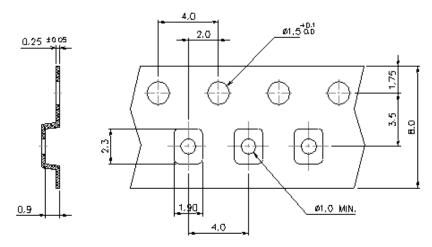
Recommended Circuit



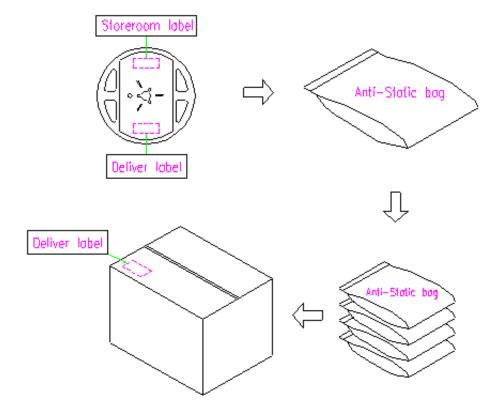
Reel Dimension



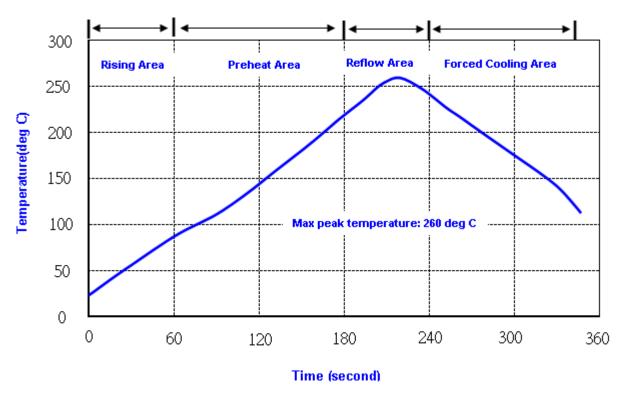
Tape Dimensions (mm)



Packing Quantity/Packing: 3K pcs maximum per reel



Reflow Profile:



Notes of the Usage:

- 1. Touch the solder iron at 260+/-5 deg C onto the leads for 10+/-2 sec max or touch the solder at 350+/-5 deg C onto the leads for 3+/-0.5 sec.
- 2. In the customer's reflow process, if it will remain some mechanical stress at the soldering terminals, also make some cracks on the soldering termination. Some cracks will cause open or short circuit and cause of thermal increasing or smoking. Don't make any excess mechanical stress to soldering points.
- In case of giving a heavy shock to the products, it may make an open or short circuit and cause of thermal increasing and smoking. To avoid heavy shock impact applying to products is strictly required.
- 4. Ultrasonic cleaning should be avoided to prevent damage to the TCXO.
- 5. Do Not Use Ultrasonic-Wave Soldering or Wave Solder with Package Immersed in Solder.
- 6. Do not lay out the ground (GND) pattern under crystal unit, this will add parasitic capacitance.
- 7. Do not run Digital / RF signal lines, power, or ground under oscillators for multi-layered PCB, as this will add noise.

Notes of the Storage:

- 1. To keep products under the condition at the room temperature (-5~35 deg C) with normal humidity (45~75%). Absorption of moisture and dewdrop may make inferiority of characteristics and a short circuit.
- Oxidization of terminals shall make the solderability more inferior. Dusts and corrosive gas will make a cause of the open or short circuit. Keep it in the clean place where is not in dusty and no corrosive gas.
- 3. Use the unti-static material to the storage package.
- 4. Don't put any excess weight to the TCXO in the storage process.
- 5. Don't move the product from the cold place to the hot place in the short time, otherwise it may make some dew-drop, then a short circuit may happen in case.
- Storage periods should be maximum 6 months under condition of above item 1 after delivery from TST factory.
- 7. Once open the bag, there is possibility of electrical characteristics deterioration due to absorption of moisture. So, please use parts within 7 days after opening the bag.
- 8. If you have to keep parts without using after opening the bag, please put the drying agent in the bag, fold the bag and keep it in the place where temperature and humidity are controlled (nitrogen atmosphere box etc.)

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Reliability Specifications

Test name	Test process / method	Reference standard					
Mechanical characteristics							
resistance to Soldering heat (IR reflow)	Temp./ Duration: 265°C /10sec x2 times Total time: 4min.(IR-reflow)	EIAJED-4701 -300(301)M(II)					
Vibration	Total peak amplitude : 1.5mm Vibration frequency : 10 to 2000 Hz Sweep period : 20 minute Vibration directions : 3 mutually perpendicular Duration : 2 hr / direc.	MIL-STD 202G method 204					
Mechanical Shock	directions : 3 impacts per axis Acceleration : 3000g's, +20/-0 % Duration : 0.3 ms (total 18 shocks) Waveform : Half-sine	MIL-STD 202G method 213					
Solderability	Solder Temperature:265±5°C Duration time: 5±0.5 seconds.	J-STD-002					
Environmental	characteristics	•					
Thermal Shock	Heat cycle conditions -40 $^{\circ}$ C (30min) \longleftrightarrow 85 $^{\circ}$ C (30min) * cycle time : 10 times	MIL-STD 883G method 1010.8					
Humidity test	Temperature: 85 ± 2 °C Relative humidity: 85% Duration: 96 hours	MIL-STD 202G method 103					
Dry heat (Aging test)	Temperature : 125 ± 2 °C Duration : 168 hours	MIL-STD 202G method 108A					
Cold resistance (Low Temp Storage)	Temperature : -40 ± 2 °C Duration : 96 hours	IEC 60068-2-1					